At page 12, line 23, please change "60" to --160--.

At page 12, line 25, please change "62" to --162-- and "64" to --164--.

At page 15, line 1, please change "right" to --left--.

At page 15, line 2, please change "left" to --right--.

At page 19, line 1, please change "request," to --request 203,--.

REMARKS

Via this amendment, several corrections and minor amendments are made to the specification and drawings to correct obvious errors therein, and to eliminate the multiple use of several reference characters that were discovered during the preparation of formal drawings. Also enclosed is a set of formal drawings, and a marked up set of informal drawings with changes marked in red.

If there are any questions concerning the foregoing, the Examiner is invited to contact the undersigned at the number listed below.

Respectfully submitted,

STEVEN M. BLUMENAU et al.

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Attorney's Docket No.: E0295/7066
Date: August 6, 1999

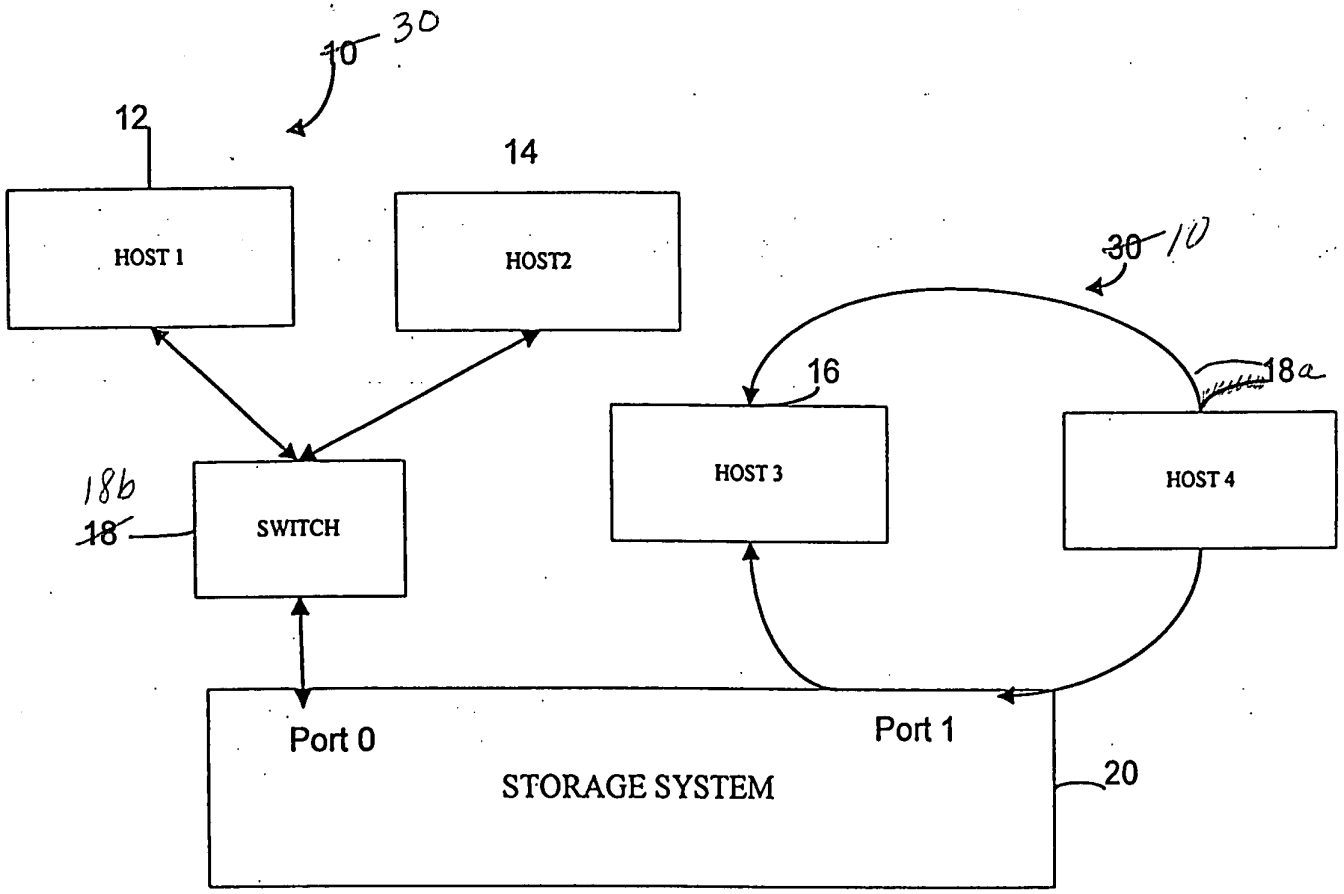

10
30
12
14
HOST 1
HOST2
30
10
16
18a
HOST 3
HOST 4
18b
18
SWITCH
Port 0
Port 1
STORAGE SYSTEM
20

FIG. 1C

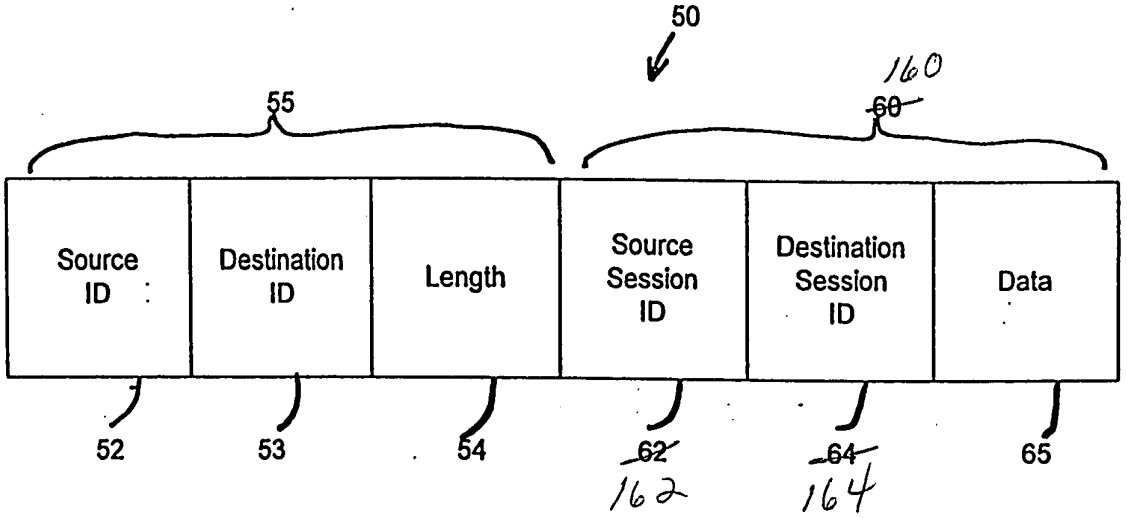

50
160
55
60
Source ID
Destination ID
Length
Source Session ID
Destination Session ID
Data
52
53
54
62
64
65
162
164

FIG. 2

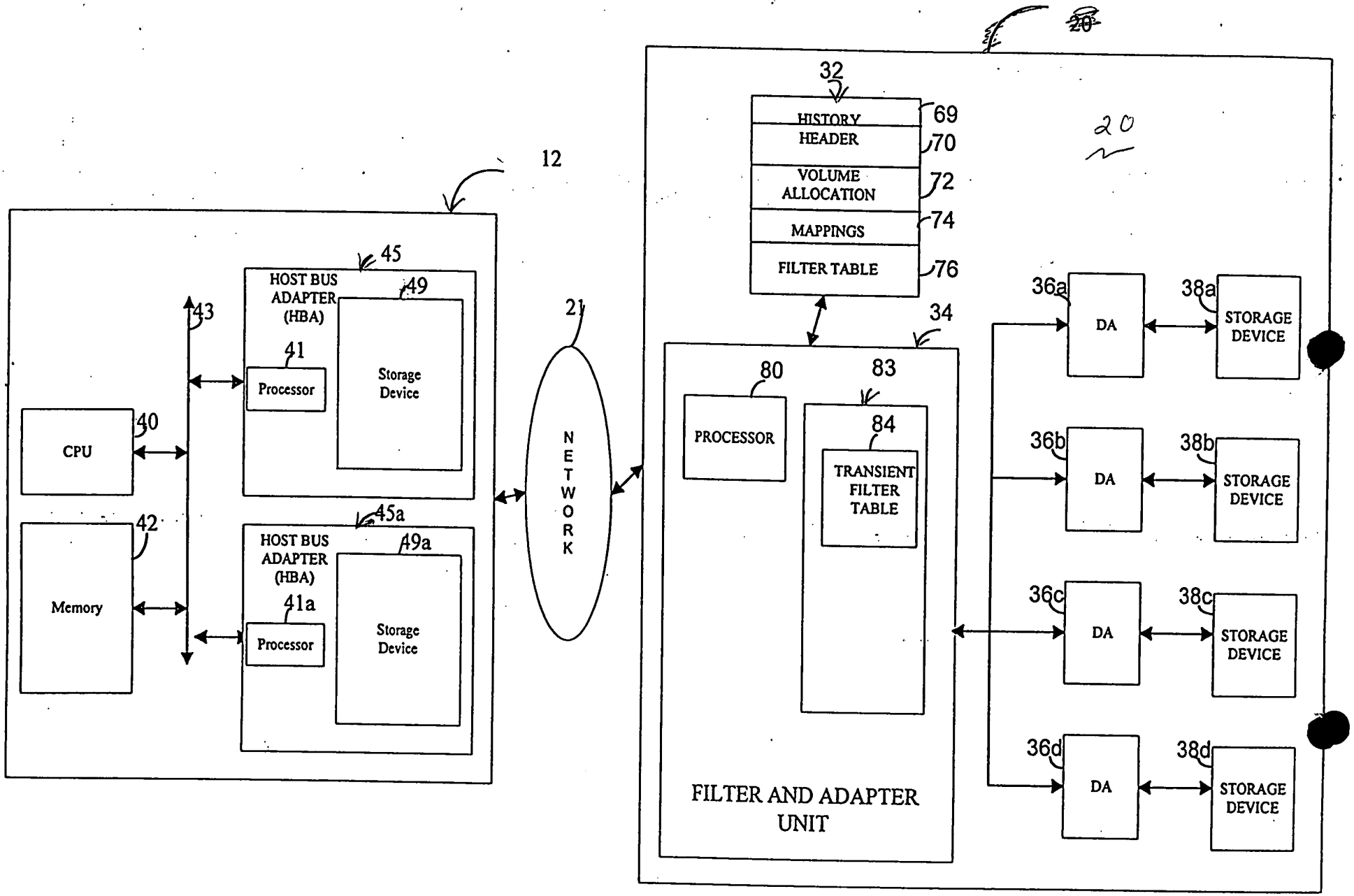

12
20
32
HISTORY
69
HEADER
70
VOLUME ALLOCATION
72
MAPPINGS
74
FILTER TABLE
76
45
HOST BUS ADAPTER (HBA)
49
41
Processor
Storage Device
43
21
NETWORK
CPU
40
Memory
42
45a
HOST BUS ADAPTER (HBA)
49a
41a
Processor
Storage Device
34
80
PROCESSOR
83
84
TRANSIENT FILTER TABLE
FILTER AND ADAPTER UNIT
36a
DA
38a
STORAGE DEVICE
36b
DA
38b
STORAGE DEVICE
36c
DA
38c
STORAGE DEVICE
36d
DA
38d
STORAGE DEVICE

FIG. 3

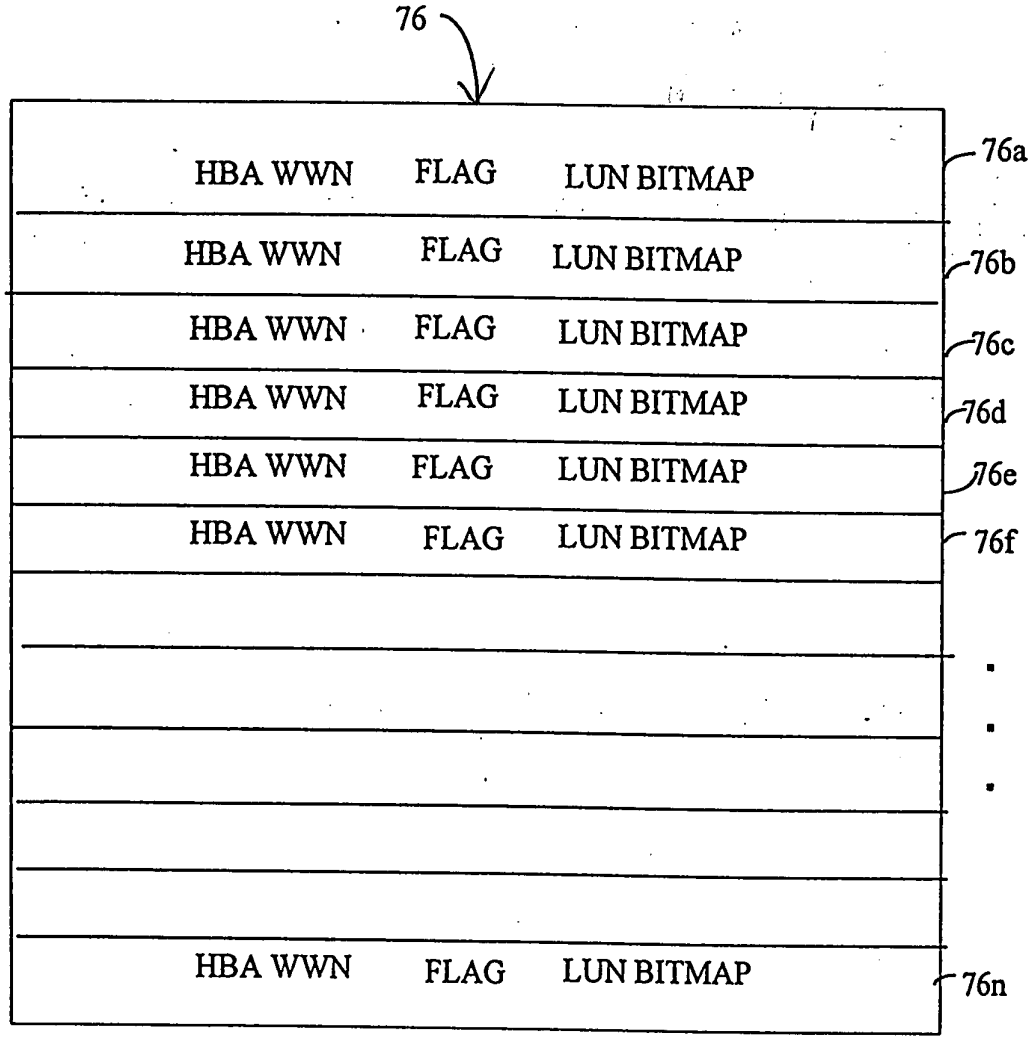

76
HBA WWN FLAG LUN BITMAP
76a
HBA WWN FLAG LUN BITMAP
76b
HBA WWN FLAG LUN BITMAP
76c
HBA WWN FLAG LUN BITMAP
76d
HBA WWN FLAG LUN BITMAP
76e
HBA WWN FLAG LUN BITMAP
76f
HBA WWN FLAG LUN BITMAP
76n

FIG. 4

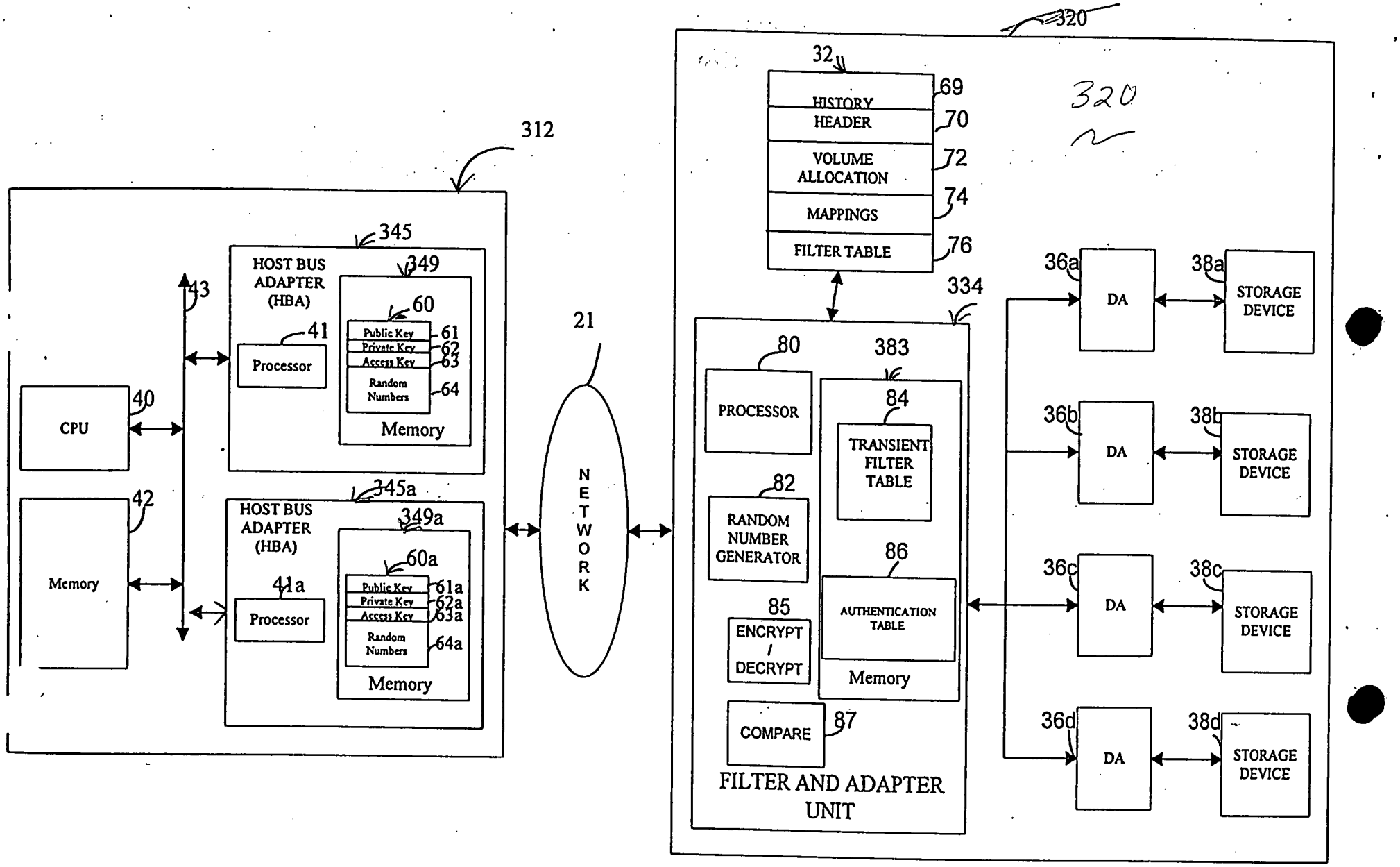

320
32
HISTORY
HEADER
VOLUME ALLOCATION
MAPPINGS
FILTER TABLE
69
70
72
74
76
320
312
345
HOST BUS ADAPTER (HBA)
349
60
Public Key
Private Key
Access Key
Random Numbers
61
62
63
64
Memory
41
Processor
43
CPU
40
Memory
42
345a
HOST BUS ADAPTER (HBA)
349a
60a
Public Key
Private Key
Access Key
Random Numbers
61a
62a
63a
64a
Memory
41a
Processor
21
NETWORK
334
80
PROCESSOR
383
84
TRANSIENT FILTER TABLE
82
RANDOM NUMBER GENERATOR
86
AUTHENTICATION TABLE
85
ENCRYPT / DECRYPT
Memory
COMPARE
87
FILTER AND ADAPTER UNIT
36a
DA
38a
STORAGE DEVICE
36b
DA
38b
STORAGE DEVICE
36c
DA
38c
STORAGE DEVICE
36d
DA
38d
STORAGE DEVICE

FIG. 6

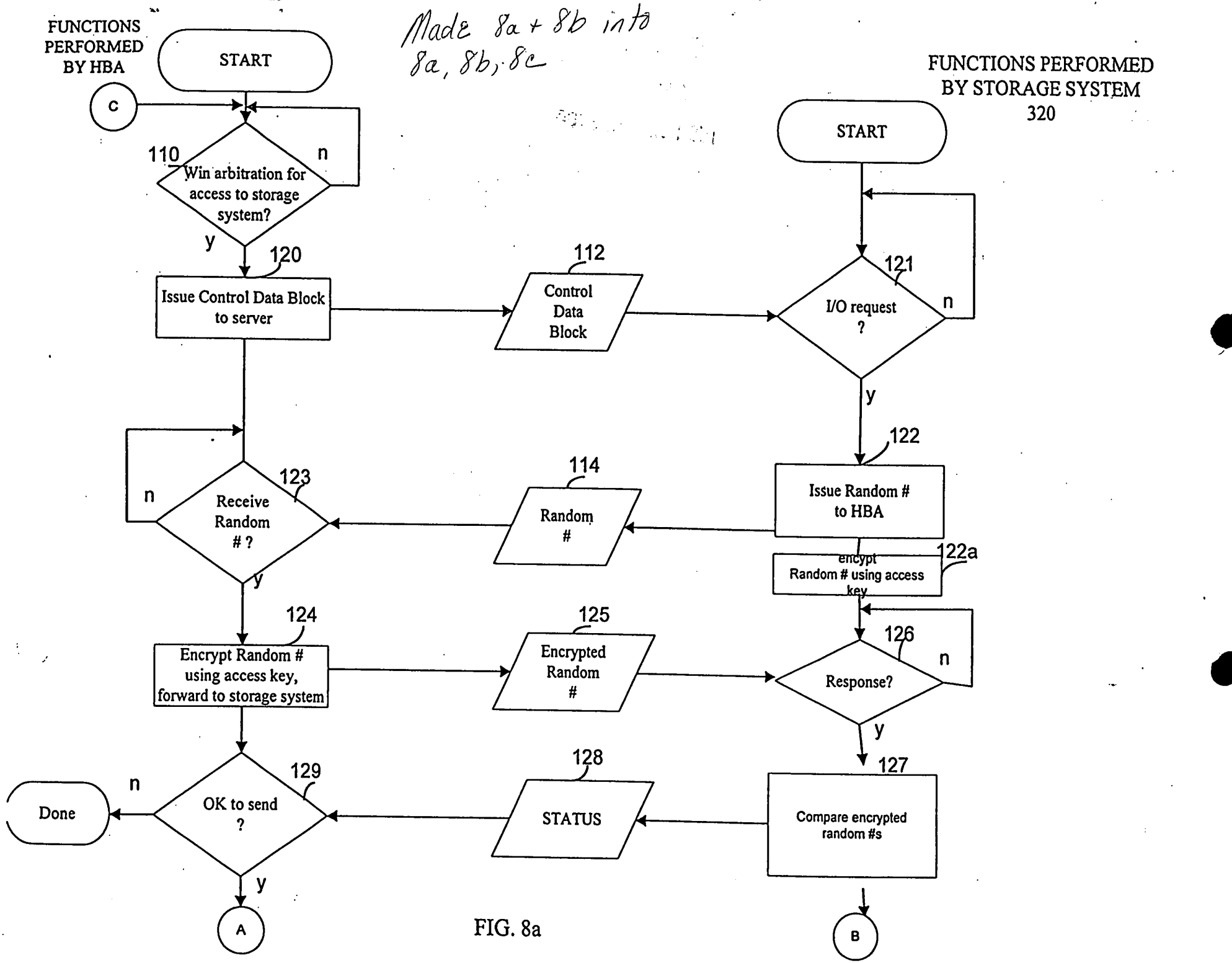

FUNCTIONS PERFORMED BY HBA
START
c
110
Win arbitration for access to storage system?
n
y
120
Issue Control Data Block to server
112
Control Data Block
FUNCTIONS PERFORMED BY STORAGE SYSTEM 320
START
121
I/O request ?
n
y
122
Issue Random # to HBA
122a
encypt Random # using access key
114
Random #
123
Receive Random # ?
n
y
124
Encrypt Random # using access key, forward to storage system
125
Encrypted Random #
126
Response?
n
y
127
Compare encrypted random #s
128
STATUS
129
OK to send ?
n
Done
y
A
B

FIG. 8a

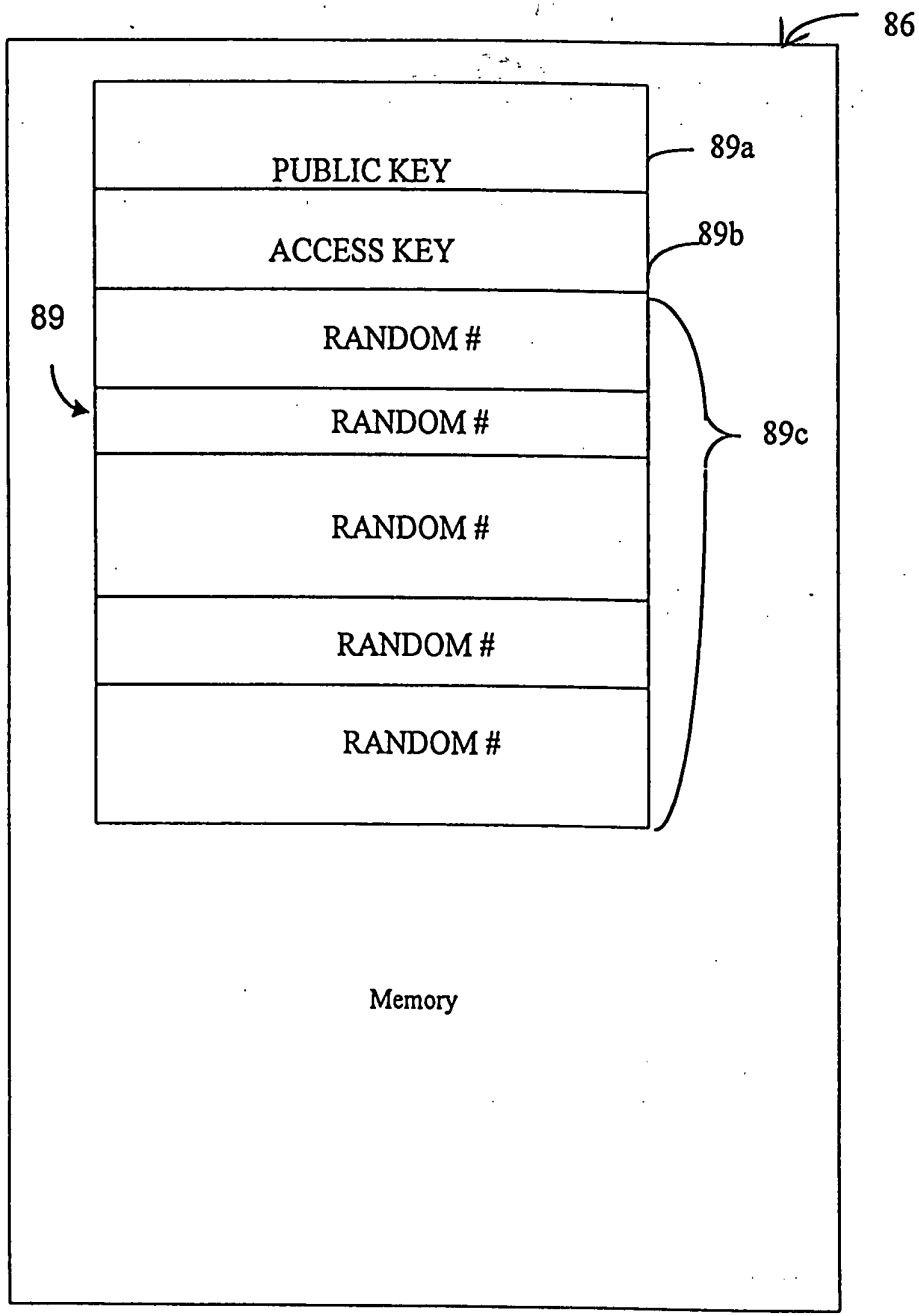
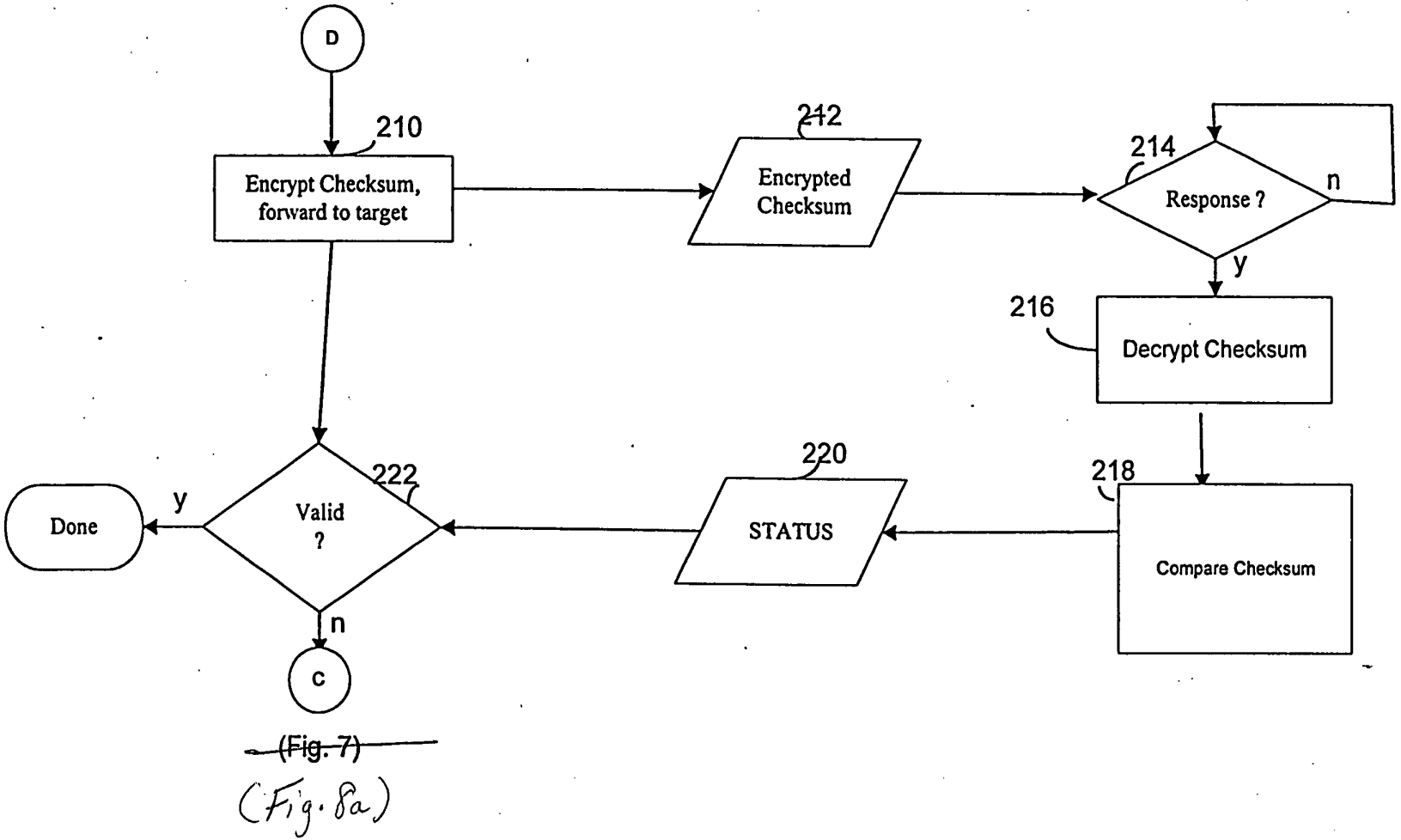

86
PUBLIC KEY
89a
ACCESS KEY
89b
89
RANDOM #
RANDOM #
89c
RANDOM #
RANDOM #
RANDOM #
Memory

FIG. 9



~~(Fig. 7)~~

(Fig. 8a)

FIG. 11